

1.9.0.7330 -- Release Notes ... In the list of options available for integration, it says that it supports: Windows 10, Windows 8.1, Windows 8, Windows 7 SP1, Windows Server 2016, Windows Server 2012 R2, Windows Server 2012, Windows Server 2008, Windows Server 2008 R2, and Windows 7. Can anyone confirm this? If so, how can I determine which versions this actually includes? A: From the NTLite's Documentation: NTLite supports Windows 10, Windows 8.1, Windows 8, Windows 7 SP1, Windows Server 2016, Windows Server 2012 R2, Windows Server 2012, Windows Server 2008 R2, Windows Server 2008, Windows Server 2008 SP2, Windows Server 2003 R2 SP2, Windows Server 2003, Windows Server 2000 SP4 SP4, Windows Server 2000 SP3 and Windows 2000 SP3. References Documentation The Basics of Windows Integration NTLite Help and Documentation 1. Field of the Invention This invention generally relates to the fabrication of integrated circuits, and more specifically, to a method and circuit for providing a direct write access to a page of cells in a one-time-programmable memory. 2. Description of the Prior Art FIG. 1 shows a schematic representation of a conventional memory 100. Memory 100 includes a memory cell array 101, a pair of word lines 12a and 12b, a pair of bit lines 14a and 14b, a pair of sense amplifiers 15a and 15b, a pair of input buffers 16a and 16b, and a pair of output buffers 17a and 17b. The memory cell array 101, including memory cells 20, is divided into a plurality of columns and a plurality of rows. Each row includes a plurality of memory cells 20 connected between a word line 12a or 12b and a bit line 14a or 14b. The word lines 12a and 12b are parallel to and run through each of the columns of memory cell array 101. Bit lines 14a and 14b are parallel to and run through each of the rows of memory cell array 101. Sense amplifiers 15a and 15b are connected to bit lines 14a and 14b respectively, and serve to detect and amplify the charge stored in memory cells 20 and to transmit the amplified signal to a pair of input buffers 16a and 16b. Input buffers 16a and 16b are connected to each other

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